



06-03-05

RECEIPT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09/595776	Docket Number	MIPS.0166-00-US
Filed	06/16/00	Group Art Unit	2183
Examiner	DAVID J. HUISMAN	Customer No.	23669
Application Title	INSTRUCTION FETCHING SYSTEM IN A MULTITHREADED PROCESSOR UTILIZING CACHE MISS PREDICTIONS TO FETCH INSTRUCTIONS FROM MULTIPLE HARDWARE STREAMS		
First Named Inventor	ENRIC MUSOLL		

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Filing Receipt dated 08/23/2000 incorrectly indicates the domestic priority data as claimed in the above noted application. Please correct the Continuing Data As Claimed by Applicant as follows.

THIS APPLICATION IS A CIP OF 09/216,017 12/16/1998

AND A CIP OF 09/240,012 01/29/1999

AND A CIP OF 09273,810 03/22/1999

WHICH IS A CIP OF 09/216,017 12/16/1998

AND A CIP OF 09/240,012 01/29/1999

THIS APPLICATION IS ALSO A CIP OF 09/312,302 05/19/1999

WHICH IS A CIP OF 09/216,017 12/16/1998

AND A CIP OF 09/240,012 01/27/1999

AND A CIP OF 09/273,810 03/22/1999.

A copy of the Filing Receipt with corrective markings thereon is attached hereto along with a copy of the first page of the application as filed which correctly claims domestic priority of the four above-noted applications.

EXPRESS MAIL LABEL NUMBER: **EO 004 903 893 US**

DATE OF DEPOSIT: 6/1/05

I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to Mail Stop **MISSING PARTS**, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Respectfully submitted,
HUFFMAN PATENT GROUP, LLC

By


RICHARD K. HUFFMAN

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Date

6/1/05

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FILING RECEIPT



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UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark OfficeAddress: ASSISTANT SECRETARY AND
COMMISSIONER OF PATENT AND TRADEMARKS
Washington, D.C. 20231

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/595,776	06/16/2000	2782	345	P3810	4	13	3

24739
CENTRAL COAST PATENT AGENCY
PO BOX 187
AROMAS, CA 95004

Date Mailed: 08/23/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Enric Musoll, San Jose, CA ;
Mario Nemirovsky, Saratoga, CA ;

Continuing Data as Claimed by Applicant

THIS APPLICATION IS A CIP OF 09/216,017 12/16/1998

~~AND WHICH IS A CIP OF 09/240,012 01/29/1999~~~~AND WHICH IS A CIP OF 09/273,810 03/22/1999~~

WHICH IS A CIP OF 09/216,017 12/16/1998

AND A CIP OF 09/240,012 01/29/1999

This application is also a CIP OF 09/312302 05/19/1999

Foreign Applications: which is a CIP of 09/216017 12/16/1998

AND a CIP OF 09/240012 1/27/1999

If Required, Foreign Filing License Granted 08/22/2000

** SMALL ENTITY **

AND a CIP OF 09273810 3/22/1999

Title

Methods and apparatus for improving fetching and dispatch of instructions in multithreaded processors

Preliminary Class

712

Data entry by : WARREN, EFREM

Team : OIPE

Date: 08/23/2000





Methods and Apparatus for Improving Fetching and Dispatch of Instructions in Multithreaded Processors

by inventors Enric Musoll and Mario Nemirovsky

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Field of the Invention

The present invention is in the area of microprocessors, and pertains more particularly to structure and function of simultaneous multithreaded processors.

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Cross Reference to Related Documents

The present application is a continuation-in-part (CIP) of prior co-pending patent applications 09/216,017, filed 12/16/98, 09/240,012, filed 1/27/99, 09/273,810, filed 3/22/99 and 09/312,302 filed 5/14/99 all four of which are incorporated herein in their entirety by reference.

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Background of the Invention

Multi-streaming processors capable of processing multiple threads are known in the art, and have been the subject of considerable research and development. The present invention takes notice of the prior work in this field, and builds upon that work, bringing new and non-obvious improvements in apparatus and methods to the art. The inventors have provided with this patent application an Information Disclosure Statement listing a number of published papers in the technical field of multi-streaming processors, which together provide additional background and context for the several aspects of the present invention disclosed herein.

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